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| AMENDMENT | Application # | 09/856,361 |
| | Confirmation # | 6882 |
| | Filing Date | May 22, 2001 |
| | First Inventor | KEZUKA |
| | Art Unit | 1763 |
| | Examiner | Goudreau, George A. |
| | Docket # | P07222US00/BAS |

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

S I R:

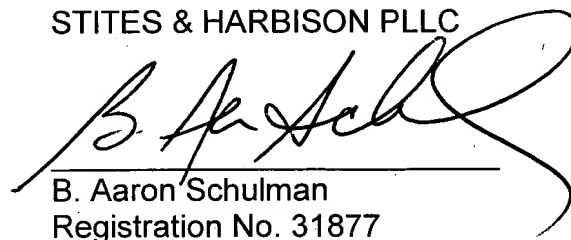
In response to the Office Action dated July 27, 2004, please consider the responsive **Remarks** provided herewith in **Attachment A**, and please amend the above identified application as follows:

- **Amendments to the Claims** are reflected in the listing of the claims provided herewith in **Attachment B**.
- A reference entitled ""Next Generation Very-Large-Scale Integrated Circuit Technology" and partial English translation are provided in **Attachment C**.

In view of the amendments made and the remarks provided, it is submitted that the present application is in condition for allowance.

Respectfully submitted,

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January 11, 2005